

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Levent Gulari	Dated: October 12, 2010
Serial Number: 10/596,569	Examiner: Nathan W. Ha
Filing date: June 16, 2006	Group Art Unit: 2814
Title: CONTOURED INSULATOR LAYER OF SILICON-ON-INSULATOR WAFERS AND PROCESS OF MANUFACTURE	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

AMENDMENT UNDER 37 C.F.R. § 1.111

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Sir:

In response to the Office Action dated **July 9, 2010**, Applicant respectfully requests reconsideration and allowance of the above-identified application as follows:

Amendments to the Claims are reflected in the Listing of Claims beginning on page 2 of this paper.

Remarks begin on page 6 of this paper.